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ATTORNEY DOCKET NO. 03180.0326
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Mail Stop Patent Application

Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

New U.S. Patent Application

Title: SEMICONDUCTOR DEVICE AND METHOD OF
MANUFACTURING THE SAME

Inventors' Names and Addresses:

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Sir:

We enclose the following papers for filing in the United States Patent and Trademark Office in connection with the above patent application.

1. Application- 23 pages, including 3 independent claims and 17 claims total.

2. Drawings- 13 sheets of drawings containing 21 figures (Figures 1-21).
3. Certified copy of Japanese Patent Application No. 2002-338452, filed November 21, 2002.
4. Information Disclosure Statement and Information Disclosure Citation, PTO 1449 with 4 documents attached.
5. The filing fee is calculated as follows:

Basic Application Filing Fee					\$750	\$ \$750.00
	Number of Claims		Basic	Extra Claims		
Total Claims	17	-	20	0	x \$18	\$
Independent Claims	3	-	3	0	x \$84	\$
<input type="checkbox"/> Presentation of Multiple Dep. Claim(s)					+\$280	
Subtotal						\$ 750.00
Reduction by 1/2 if small entity						-
TOTAL APPLICATION FILING FEE						\$ 750.00

6. A check for \$750.00 is enclosed. The fee includes:

\$750.00 filing fee

This application is being filed under the provisions of 37 C.F.R. § 1.53(f). Applicants await notification from the Patent and Trademark Office of the time set for filing the Declaration.

Applicants claim the right to priority based on Japanese Patent Application No. 2002-338452, filed November 21, 2002.

Please address all correspondence with respect to this application to:

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Please accord this application an application number and filing date.

Commissioner of Patents
July 3, 2003
Page 3

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The Commissioner is hereby authorized to charge any additional filing fees due and any other fees due under 37 C.F.R. § 1.16 or §1.17 during the pendency of this application to our Deposit Account No. 06-0916.

Respectfully submitted,

FINNEGAN, HENDERSON, FARABOW,
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By: 

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RVB/FPD/cgb
Enclosures

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